WEST Search History

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DATE: Thursday, February 19, 2004

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		DB=USPT; PLUR=YES; OP=ADJ	
☐	L10	L8 near10 word	19
☐	L8	demultiplex$ near3 block	856
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☐	L7	L6.ab,ti.	8
☐	L6	demultiplex$ near3 block	275
☐	L5	20020095536	1
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☐	L3	demultiplex$ near3 block near10 serial$	30
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L7: Entry 7 of 8 File: PGPB May 22, 2003

DOCUMENT-IDENTIFIER: US 20030095057 A1
TITLE: Hybrid parallel/serial bus interface

Abstract Paragraph:
A hybrid serial/parallel bus interface has a data block demultiplexing device. The data block demultiplexing device has an input configured to receive a data block and demultiplexes the data block into a plurality of nibbles. For each nibble, a parallel to serial converter converts the nibble into serial data. A line transfers each nibble's serial data. A serial to parallel converter converts each nibble's serial data to recover that nibble. A data block reconstruction device combines the recovered nibbles into the data block.

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L7: Entry 6 of 8 File: PGPB Jun 5, 2003

DOCUMENT-IDENTIFIER: US 20030105893 A1
TITLE: Method employed by a user equipment for transferring data

Abstract Paragraph:
A hybrid serial/parallel bus interface method for a user equipment (UE) has a data block demultiplexing device. The data block demultiplexing device has an input configured to receive a data block and demultiplexes the data block into a plurality of nibbles. For each nibble, a parallel to serial converter converts the nibble into serial data. A line transfers each nibble's serial data. A serial to parallel converter converts each nibble's serial data to recover that nibble. A data block reconstruction device combines the recovered nibbles into the data block.

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L7: Entry 5 of 8 File: PGPB Jun 5, 2003

DOCUMENT-IDENTIFIER: US 20030105894 A1
TITLE: Method employed by a base station for transferring data

Abstract Paragraph:
A hybrid serial/parallel bus interface method for a base station has a data block demultiplexing device. The data block demultiplexing device has an input configured to receive a data block and demultiplexes the data block into a plurality of nibbles. For each nibble, a parallel to serial converter converts the nibble into serial data. A line transfers each nibble's serial data. A serial to parallel converter converts each nibble's serial data to recover that nibble. A data block reconstruction device combines the recovered nibbles into the data block.

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L7: Entry 4 of 8 File: PGPB Jun 5, 2003

DOCUMENT-IDENTIFIER: US 20030105895 A1
TITLE: User equipment having a hybrid parallel/serial bus interface

Abstract Paragraph:
A hybrid serial/parallel bus interface for a user equipment (UE) has a data block demultiplexing device. The data block demultiplexing device has an input configured to receive a data block and demultiplexes the data block into a plurality of nibbles. For each nibble, a parallel to serial converter converts the nibble into serial data. A line transfers each nibble's serial data. A serial to parallel converter converts each nibble's serial data to recover that nibble. A data block reconstruction device combines the recovered nibbles into the data block.

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L5: Entry 1 of 1 File: PGPB Jul 18, 2002

DOCUMENT-IDENTIFIER: US 20020095536 A1
TITLE: Parallel read/write circuit and method for efficient storing/retrieval of data to/from a recording medium

Pre-Grant Publication (PGPub) Document Number:
20020095536

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L3: Entry 3 of 30 File: USPT Dec 17, 2002

DOCUMENT-IDENTIFIER: US 6496540 B1
TITLE: Transformation of parallel interface into coded format with preservation of baud-rate

Brief Summary Text (9):
A method of coding parallel data for transmission while maintaining baud rate in accordance with the present invention includes the steps of providing a plurality of uncoded data blocks having a predetermined baud rate, demultiplexing the data blocks to sequentially distribute the data blocks to encoders, encoding the data blocks at the demultiplexed rate and serializing the coded data blocks for serially transmitting data at the predetermined baud rate.